

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (original): A printer control circuit, which is a dedicated printer control hardware circuit disposed between an upper apparatus and a printer, comprising:

a halftoning circuit for performing a halftone process to convert high-resolution raster data, for a first image element, that are transmitted from said upper apparatus into first low-resolution raster data; and

an image completion circuit for obtaining low-resolution raster data for a complete print image employing second low-resolution raster data, for a second image element, that are transmitted from said upper apparatus, and said first low-resolution raster data, for said first image element, that are transmitted from said halftoning circuit.

2. (original): A printer control circuit according to claim 1, wherein said first image element is an illustration, and said second image element constitutes characters and graphics.

3. (original): A printer control circuit according to claim 1 or 2, wherein said high-resolution raster data for said first image element, which are transmitted from said upper apparatus, are expressed using an upper apparatus display color system that differs from a printer display color system that is employed by said printer; wherein said second low-resolution raster data for said second image element, which are transmitted from said upper apparatus, are expressed using said printer display color system; and wherein said halftoning circuit also

performs color conversion for said high-resolution raster data for said first image element that are transmitted from said upper apparatus display color system to said printer display color system.

4. (original): A printer control circuit according to claim 1, wherein for said low-resolution raster data for said complete print image said image completion circuit changes pixel order for interlaced printing.

5. (original): A printer control apparatus according to claim 1, wherein a memory is provided for said image completion circuit, and wherein to obtain said low-resolution raster data for said complete print image said first low-resolution raster data for said first image element and said second low-resolution raster data for said second image element are superimposed and are written in said memory.

6. (original): A printer control apparatus according to claim 5, wherein, when said image completion circuit is writing one of said first low-resolution raster data for said first image element and said second low-resolution raster data for said second image element, said image completion circuit holds the other of said first and second low-resolution raster image data that are to be written to said memory.

7. (original): A printer control apparatus according to claim 5, wherein said high-resolution raster data for said first image element and said second low-resolution raster data for said second image element are sequentially transmitted by said upper apparatus; and wherein, when said image completion circuit recognizes that raster data both for said first and for said

second image elements have been rasterized, said image completion circuit increments a vertical address for designating a location in said memory for writing said raster data, and superimposes and writes, at the same vertical address in said memory, said raster data for said first and said second image elements for the same raster.

8. (original): A printer control apparatus according to claim 7, wherein, for a raster having said first image element or said second element not available, a raster end command for instructing raster termination of a pertinent image element is transmitted by said upper apparatus, and wherein, upon receiving said raster end command, said image completion circuit acknowledges said raster termination of said pertinent image element and obtains the same results without requiring null data for said pertinent image element that is being written in said memory.

9. (original) A printer control apparatus according to claim 5, wherein, in order to develop said low-resolution raster data for said complete print image, said memory has a capacity that is large enough to store all the raster data in a range that is equivalent to one where the print head of said printer covers two paths; and wherein, while said image completion circuit reads raster data stored in said memory that said print head requires for the current path, said image completion circuit writes raster data in said memory until the last raster that said print head requires for the next path is reached.

10. (original): A printer control apparatus according to claim 5, wherein said image completion circuit writes raster data to said memory in an OR write mode; and wherein said image completion circuit reads raster mode from said memory in a clear read mode during the

last reading cycle for each raster, and in a normal read mode during a reading cycle other than the last reading cycle.

11. (original) A printer comprising:

a dedicated hardware circuit for processing image data that are received from an upper apparatus, said dedicated hardware circuit including

a halftoning circuit for performing halftoning for high-resolution raster data for a first image element received from said upper apparatus and obtaining first low-resolution raster data; and

an image completion circuit for obtaining low-resolution raster data for a complete print image from second low-resolution raster data that are received from said upper apparatus for a second image element, and said first low-resolution raster data that are received from said halftoning circuit for said first image element,

wherein printing is performed using said low-resolution raster data that are obtained by said dedicated hardware circuit for said complete print image.

12. (original): A printer according to claim 11, wherein said first image element is an illustration, and said second image element constitutes characters and graphics.

13. (original): A printer according to claim 11 or 12, wherein said high-resolution raster data for said first image element, which are transmitted from said upper apparatus, are expressed using an upper apparatus display color system that differs from a printer display color system that is employed by said printer; wherein said second low-resolution raster data for said second image element, which are transmitted from said upper apparatus, are expressed using said printer

display color system; and wherein said halftoning circuit also performs color conversion for said high-resolution raster data for said first image element that are transmitted from said upper apparatus display color system to said printer display color system.

14. (original): A printer according to claim 11, wherein for said low-resolution raster data for said complete print image said image completion circuit changes pixel order for interlaced printing.

15. (original): A printer according to claim 11, wherein a memory is provided for said image completion circuit, and wherein to obtain said low-resolution raster data for said complete print image said first low-resolution raster data for said first image element and said second low-resolution raster data for said second image element are superimposed and are written in said memory.

16. (original): A printer according to claim 15, wherein, when said image completion circuit is writing one of said first low-resolution raster data for said first image element and said second low-resolution raster data for said second image element, said image completion circuit holds the other of said first and second low-resolution raster image data that are to be written to said memory.

17. (original): A printer according to claim 15, wherein said high-resolution raster data for said first image element and said second low-resolution raster data for said second image element are sequentially transmitted by said upper apparatus; and wherein, when said image completion circuit recognizes that raster data both for said first and for said second image

elements have been rasterized, said image completion circuit increments a vertical address for designating a location in said memory for writing said raster data, and superimposes and writes, at the same vertical address in said memory, said raster data for said first and said second image elements for the same raster.

18. (original): A printer according to claim 17, wherein, for a raster having said first image element or said second element not available, a raster end command for instructing raster termination of a pertinent image element is transmitted by said upper apparatus, and wherein, upon receiving said raster end command, said image completion circuit acknowledges said raster termination of said pertinent image element and obtains the same results without requiring null data for said pertinent image element that is being written in said memory.

19. (original): A printer according to claim 15, wherein, in order to develop said low-resolution raster data for said complete print image, said memory has a capacity that is large enough to store all the raster data in a range that is equivalent to one where the print head of said printer covers two paths; and wherein, while said image completion circuit reads raster data stored in said memory that said print head requires for the current path, said image completion circuit writes raster data in said memory until the last raster that said print head requires for the next path is reached.

20. (original): A printer according to claim 15, wherein said image completion circuit writes raster data to said memory in an OR write mode; and wherein said image completion circuit reads raster mode from said memory in a clear read mode during the last reading cycle for each raster, and in a normal read mode during reading cycle other than the last reading cycle.

21. (original): A printing system comprising:

an upper apparatus;

a printer; and

a dedicated printer control hardware circuit located in between said upper apparatus and said printer,

said dedicated hardware circuit including

a halftoning circuit for performing halftoning for high-resolution raster data for a first image element received from said upper apparatus and obtaining first low-resolution raster data, and

an image completion circuit for obtaining low-resolution raster data for a complete print image from second low-resolution raster data that are received from said upper apparatus for a second image element, and said first low-resolution raster data that are received from said halftoning circuit for said first image element.

22. (original): A printing system according to claim 21, wherein said first image element is an illustration, and said second image element constitutes characters and graphics.

23. (original): A printing system according to claim 21 or 22, wherein said high-resolution raster data for said first image element, which are transmitted from said upper apparatus, are expressed using an upper apparatus display color system that differs from a printer display color system that is employed by said printer; wherein said second low-resolution raster data for said second image element, which are transmitted from said upper apparatus, are expressed using said printer display color system; and wherein said halftoning circuit also

performs color conversion for said high-resolution raster data for said first image element that are transmitted from said upper apparatus display color system to said printer display color system.

24. (original): A printing system according to claim 21, wherein for said low-resolution raster data for said complete print image said image completion circuit changes pixel order for interlaced printing.

25. (original): A printing system according to claim 21, wherein a memory is provided for said image completion circuit, and wherein to obtain said low-resolution raster data for said complete print image said first low-resolution raster data for said first image element and said second low-resolution raster data for said second image element are superimposed and are written in said memory.

26. (original): An upper apparatus, for a printing system for outputting image data to be printed by a printer, that outputs high-resolution raster data for a first image element included in a print image and low-resolution raster data for which halftoning has been processed for a second image element included in said print image.

27. (original): An upper apparatus according to claim 26, wherein said first image element is an illustration, and said second image element constitutes characters and graphics.

28. (original): An upper apparatus according to claim 26, which expresses said high-resolution raster data for said first image element using an upper apparatus display color system that differs from a printer display color system that is employed by said printer, and which

expresses said low-resolution raster data for said second image element using said printer display color system.

29. (original): An upper apparatus according to claim 26, which sequentially transmits said high-resolution raster image for said first image element and said low-resolution raster data for said second image element, and which, for a raster having said first image element or said second image element not available, transmits a raster end command for instructing raster termination of a pertinent image element.

30. (original): A computer-readable recording medium on which is stored a computer program for use in a computer, said computer constituting an upper apparatus, for a printing system for outputting image data to be printed by a printer, that outputs high-resolution raster data for a first image element included in a print image and low-resolution raster data for which halftoning has been processed for a second image element included in said print image.

31. (previously presented): A printer control circuit according to claim 1, wherein the low-resolution raster data for the second image element is generated by performing a halftone process in said upper apparatus.

32. (previously presented): A printer according to claim 11, wherein the low-resolution raster data for the second image element is generated by performing a halftone process in said upper apparatus.

33. (previously presented): A printing system according to claim 21, wherein the low-resolution raster data for the second image element is generated by performing a halftone process in said upper apparatus.

34. (previously presented): An upper apparatus according to claim 26, wherein the low-resolution raster data for the second image element is generated by performing a halftone process in said upper apparatus.

35. (previously presented): A computer-readable recording medium according to claim 30, wherein the low-resolution raster data for the second image element is generated by performing a halftone process in the upper apparatus.

36. (new) A printer control circuit of claim 1, further comprising:
an upper apparatus interface for communicating with an upper apparatus; and
a printer interface for communicating with a printer.

37. (new) A printer control circuit of claim 36, further comprising:
a command generation circuit which converts the low-resolution raster data for the complete print image into a printer command which is transmitted to the printer via said printer interface.

38. (new) A printing system of claim 21, wherein said dedicated hardware circuit further includes a printer interface for communicating with the printer.

39. (new) A printing system of claim 38 further comprising:
a command generation circuit which converts the low-resolution raster data for the complete print image into a printer command which is transmitted to the printer via said printer interface.